
APPLICATION DATA SHEET FORM

Application Information

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Application Information

Title Line One:: Process For Forming An Integrated Circuit Comprising
Non-Volatile Memory Cells And Side Transistors Of
At Least Two Different Types, And Corresponding IC
Total Drawing Sheets:: 2
Application Type:: Non-Provisional
Docket Number:: S1022/8827

Representative Information

Representative Customer Number:: 23628

Continuity Information

This application is a:: Continuation
>Application One:: 09/123,295
Filing Date:: July 28, 1998

which is a:: Divisional
>Application Two:: 08/670,179
Filing Date:: June 20, 1996

Prior Foreign Applications

Foreign Application One:: 95830282.0
Filing Date:: June 30, 1995
Country:: Europe
Priority Claimed:: Yes

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